

ISA-PNP

User's Manual

Bear Technologies
www.beartech.com.tw

Product Feature :

- ISA Slot Test
- Support Plug and Play ISA
- **ISA-PNP Can Test Up to 16 ISA Slot**

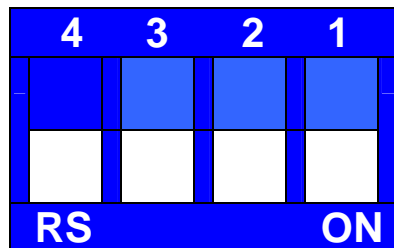
Product Content :

- ISA-PNP Test Card * 1



JUMPER SETTING,LED,SWITCH:

- SW1



- Bit 1 : ID0 (ON → ID0 = 0 , OFF → ID0 = 1)
Bit 2 : ID1 (ON → ID1 = 0 , OFF → ID1 = 1)
Bit 3 : ID2 (ON → ID2 = 0 , OFF → ID2 = 1)

Bit 4 : ID3 (ON → ID3 = 0 , OFF → ID3 = 1)

SW1	ID3	ID2	ID1	ID0
0H	0	0	0	0
1H	0	0	0	1
2H	0	0	1	0
3H	0	0	1	1
4H	0	1	0	0
5H	0	1	0	1
6H	0	1	1	0
7H	0	1	1	1
8H	1	0	0	0
9H	1	0	0	1
AH	1	0	1	0
BH	1	0	1	1
CH	1	1	0	0
DH	1	1	0	1
EH	1	1	1	0
FH	1	1	1	1

Different ISA Slot SW1 required tone is not the same ID cannot be repeated

● LED

- LED1 : ISA Slot +12V Status
- LED2 : ISA Slot -12V Status
- LED3 : ISA Slot -5V Status
- LED4 : WAIT INIT Status
- LED5 : SLEEP Status
- LED6 : ISO Status
- LED7 : WAIT CSN Status
- LED8 : CONFIG Status
- LED9 : INIT DERECT Status
- LED10 : ACTIVE MIO Status
- LED11 : RESTART Status
- LSEG : CSN NUMBER

● J6 (pin8 & pin9)

- OPEN : DISABLE ISA PNP (DEF)
- CLOSE : ENABLE ISA PNP

Common Program Syntax :

Filename : ISATST.EXE

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ISA SLOT TEST (ISA-PnP) Ver 1.0 08/30/2016 Bear Technologies (? : Help)
Lap count      : 0001      I/O      300h: 303h      S_time:08/30/2016 15:44:03
Loop count     : 0001      Memory   D6000h:D60FFh    C_time:08/30/2016 15:44:05

01> RESETDRV   : Pass      19> IRQ 10      : Skip      37> LA[23:16] MST : Pass
02> SMEMW#/R# : Pass      20> IRQ 11      : Skip      38> SA[15:0]  MST : Skip
      MEMW#/R#      21> IRQ 12      : Skip      39> SA[19:0]  CPU  : Pass
03> IOW#,IOR# : Pass      22> IRQ 14      : Skip
04> SD[15:0]  : Pass      23> IRQ 15      : Skip
05> IOCS16#   : Pass      24> REFRESH#(??h): Skip
06> MEMCS16#  : Pass      25> OSC      (??h): Skip
07> -5V       : Skip      14.318MHz
08> -12V      : Pass      26> ATCLK  (??h): Skip
09> +12V      : Pass      27> DRQ0,DACK0# : Pass
10> IOCHRDY#  : Skip      28> DRQ1,DACK1# : Pass
11> IOCHCK#   : Skip      29> DRQ2,DACK2# : Skip
12> OWS       : Skip      30> DRQ3,DACK3# : Pass
13> BALE      : Pass      31> DRQ5,DACK5# : Pass
14> IRQ 03    : Pass      32> DRQ6,DACK6# : Pass
15> IRQ 04    : Pass      33> DRQ7,DACK7# : Pass
16> IRQ 05    : Pass      34> AEN       : Pass
17> IRQ 07    : Skip      35> TC       : Pass
18> IRQ 09    : Skip      36> MASTER#,SBHE: Pass

02 ISA-PnP is Detected ...
ISA-PnP Port01 Testing....
(DDMA Addr CF80h:CFFFh)

Results ("Q" : Quit)

Pass
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ISATST [/????]IP??]

/????: Lap Count /1: Lap Count = 1
P??: P1 .. P16 P2: ISA-PnP Port_2 Test
PnP: PnP ISA Card All PnPM: PnP ISA Card Memory_Loc Only
+Z: Ows Test +N:IOCHCK# Test +I0:IOCHRDY# Test
+F: REFRESH# Test +0: OSC Test +A: ATCLK Test
\IRQ:All IRQ?? Skip \DMA:All DRQ? Skip
\2:SMEM?#/MEM?# Skip \5: IOCS16# Skip \6: MEMCS16# Skip \7:-5V Skip
\8: -12V Skip \9: +12V Skip \13: BALE Skip \39: SA[19:0] CPU Skip
\M: MASTER# Skip \M: MASTER MEMW# Skip
\L: LA[23:17],SA16 Skip \S: SA[15:0] Skip
\I03: IRQ3 Skip \I04: IRQ4 Skip \I05: IRQ5 Skip \I07: IRQ7 Skip
\I10: IRQ10 Skip \I11: IRQ11 Skip I09: IRQ9 Test I12: IRQ12 Test
I14: IRQ14 Test I15: IRQ15 Test \D00: DRQ00 Skip \D01: DRQ01 Skip
\D03: DRQ03 Skip \D05: DRQ05 Skip \D06: DRQ06 Skip \D07: DRQ07 Skip
\DR : DMA Memory Read -> I/O Write Skip
M: DRQ7 Generate Master# (Def: DRQ5)

ISATST /1 \I03 \M ; 01> .. 39> Test (IRQ3 Skip ,MASTER# Skip)
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Test Principle :

A. Product Feature:

- ISA Slot Test

+5V , -12V , +12V , -5V , SD[15:0] , IOW# , IOR# , MEMR# , MEMW# , SMEMR# , SMEMW# , IOCS16# , MEMCS16 , BALE , IRQ3 , IRQ4 , IRQ5 , IRQ7 , IRQ10 , IRQ11 , DRQ0 , DRQ1 , DRQ3 , DRQ4 , DRQ5 , DRQ6 , DRQ7 , AEN , TC , MASTER# , SBHE , SA[19:0] , LA[23:16]

B. Compatibility:

- MB , IPC

D. Testing Procedure:

1. ISA-PNP Test Card Connect To ISA Slot
2. Boot the screen to DOS platform
3. Excute **ISATST.EXE**

Section 4 : Test Results

A. The chips that have been tested:

- ITE IT8888

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